

Electronic Acknowledgement Receipt

EFS ID:	2013671
Application Number:	10595303
International Application Number:	
Confirmation Number:	7114
Title of Invention:	Method and Device For Secure, Insulated and Electrically Conductive Assembling of Treated Semiconductor Wafers
First Named Inventor/Applicant Name:	Roy Knechtel
Customer Number:	21967
Filer:	Roger L. Tate/Mildred Adesida
Filer Authorized By:	Roger L. Tate
Attorney Docket Number:	60291.000048
Receipt Date:	26-JUL-2007
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for status of Application	DOC003.PDF	91522 b2ea36d55f1dc07f2017bb5a906ea79ff 0cacbbd	no	1

Warnings:

Information:	Total Files Size (in bytes):	91522
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<p><u>New Applications Under 35 U.S.C. 111</u></p>		
<p>If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p>		
<p><u>National Stage of an International Application under 35 U.S.C. 371</u></p>		
<p>If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p>		
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